

L. Number	Hits	Search Text	DB	Time stamp
1	1002	silazane with silicon\$9	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/19 09:59
2	2771	\$20silazane with silicon\$9	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/19 09:57
3	2563	((\$20silazane with silicon\$9) and silicon	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/19 09:57
4	391	((\$20silazane with silicon\$9) and silicon) and dielectric	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/19 09:57
5	314	((\$20silazane with silicon\$9) and silicon) and dielectric) and substrate	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/19 09:58
6	262	(((\$20silazane with silicon\$9) and silicon) and dielectric) and substrate) and (conduct\$3 polysilicon)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/19 13:45
7	70	((((\$20silazane with silicon\$9) and silicon) and dielectric) and substrate) and (conduct\$3 polysilicon)) and (\$20silazane with deposit\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/19 13:46
9	3	"10273667"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/19 10:11
10	29	((((\$20silazane with silicon\$9) and silicon) and dielectric) and substrate) and (conduct\$3 polysilicon)) and (\$20silazane with dielectric)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/19 13:46
	240	dielectric adj (layer film) with (silicon adj containing)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/20 15:27
	57	(dielectric adj (layer film) with (silicon adj containing)) and ((silicon react\$3 gas\$2) near\$3 source\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/20 15:53
	57	((dielectric adj (layer film) with (silicon adj containing)) and ((silicon react\$3 gas\$2) near\$3 source\$1)) and (conduct\$3 near\$3 (layer film))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 08:48

302	((silicon adj containing) with (dielectric near3 (layer film)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 08:51
148	((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (source gas\$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 08:53
3	(((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (source gas\$2))) and silazane	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/11 09:30
3	((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with silazane)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 09:33
0	(silicon adj containing) with (silicon with silazane)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/05/19 09:48
63	(silicon adj containing) with (silicon with silazane)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 08:57
4	((silicon adj containing) with (silicon with silazane)) and nitridi\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 08:57
1	"6197628" PN.	USPAT	2002/09/23 08:58
1	"5637527" PN.	USPAT	2002/09/23 08:59
708	(silicon with silazane)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 09:08
18	dielectric with (silicon with nitridizing)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 09:03
116	semiconductor and (silicon with silazane)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 09:33
63	((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (silazane silane))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 09:34
9	(((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (silazane silane))) and (nitridation nitridization nitridizing)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 09:35

13 dielectric with (silicon with silazane)

USPAT 2003/02/11 09:28

US-PGPUB;

EPO, JPO;

DERWENT;

IBM_TDB

605 (silicon adj containing) with dielectric

USPAT 2003/02/11 09:29

US-PGPUB;

EPO, JPO;

DERWENT;

IBM_TDB

204 ((silicon adj containing) with dielectric) and (conduct\$3 adj (layer film))

USPAT 2003/02/11 09:33

US-PGPUB;

EPO, JPO;

DERWENT;

IBM_TDB

69 (((silicon adj containing) with dielectric) and (conduct\$3 adj (layer film))) and ((silicon adj containing) with (react\$4 agent ambient))

USPAT 2003/02/11 12:47

US-PGPUB;

EPO, JPO;

DERWENT;

IBM_TDB

1 "6358838".PN.

USPAT 2003/02/11 09:47

1 "5461010".PN.

USPAT 2003/02/11 09:52

1 "6214748".PN.

USPAT 2003/02/11 09:52

1 ("5567661").PN.

USPAT 2003/02/11 12:47

1 "5320875".PN.

USPAT 2003/02/11 14:04

1 "5318928".PN.

USPAT 2003/02/11 14:04

1 "5298587".PN

USPAT 2003/02/11 14:05

1 "5000113".PN.

USPAT 2003/02/11 14:05

L Number	Hits	Search Text	DB	Time stamp
	240	dielectric adj (layer film) with (silicon adj containing)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/20 15:27
	57	((dielectric adj (layer film) with (silicon adj containing)) and ((silicon react\$3 gas\$2) near3 source\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/20 15:53
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	302	(silicon adj containing) with (dielectric near3 (layer film))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 08:51
	148	((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (source gas\$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 08:53
	3	(((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (source gas\$2))) and silazane	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/02/11 09:30
	3	((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with silazane)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 09:33
	0	(silicon adj containing) with (silicon with silazane)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 08:56
	63	(silicon adj containing) with (silicon with silazane)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 08:57
	4	((silicon adj containing) with (silicon with silazane)) and nitridi\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 08:57
	1	"6197628".PN.	USPAT	2002/09/23 08:58
	1	"5637527".PN.	USPAT	2002/09/23 08:59
	708	(silicon with silazane)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 09:08
	18	dielectric with (silicon with nitridizing)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/23 09:03

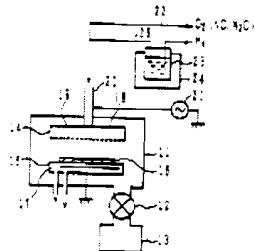
116	semiconductor and (silicon with silazane)	USPAT: 2002/09/23 09:33 US-PGPUB: EPO: JPO: DERWENT: IBM_TDB
63	((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (silazane silane))	USPAT: 2002/09/23 09:34 US-PGPUB: EPO: JPO: DERWENT: IBM_TDB
9	((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (silazane silane)) and (nitridation nitridization nitridizing)	USPAT: 2002/09/23 09:35 US-PGPUB: EPO: JPO: DERWENT: IBM_TDB
13	dielectric with (silicon with silazane)	USPAT: 2003/02/11 09:28 US-PGPUB: EPO: JPO: DERWENT: IBM_TDB
605	(silicon adj containing) with dielectric	USPAT: 2003/02/11 09:29 US-PGPUB: EPO: JPO: DERWENT: IBM_TDB
204	((silicon adj containing) with dielectric) and (conduct\$3 adj (layer film))	USPAT: 2003/02/11 09:33 US-PGPUB: EPO: JPO: DERWENT: IBM_TDB
69	((silicon adj containing) with dielectric) and (conduct\$3 adj (layer film)) and ((silicon adj containing) with (react\$4 agent ambient))	USPAT: 2003/02/11 12:47 US-PGPUB: EPO: JPO: DERWENT: IBM_TDB
1	"6358838".PN.	USPAT: 2003/02/11 09:47
1	"5461010".PN.	USPAT: 2003/02/11 09:52
1	"6214748".PN.	USPAT: 2003/02/11 09:52
1	("5567661").PN.	USPAT: 2003/02/11 12:47
1	"5320875".PN.	USPAT: 2003/02/11 14:04
1	"5318928".PN.	USPAT: 2003/02/11 14:04
1	"5298587".PN.	USPAT: 2003/02/11 14:05
1	"5000113".PN.	USPAT: 2003/02/11 14:05

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FIG. 1A



FIG. 1B



example, organic silicon materials having the structures shown in FIGS. 2A to 2D may be used. It is preferable to use silazane compound having a ring or cyclosilazane structure.

(53) O.sub.2, N.sub.2 O, and NO may be used as oxidant, and other oxidants may also be used. NF.sub.3 or NH.sub.3 may be added to the oxidant. It is apparent to those skilled in the art that various modifications, improvements, combinations and the like can be made without departing from the scope of the appended claims.

CLAIMS:

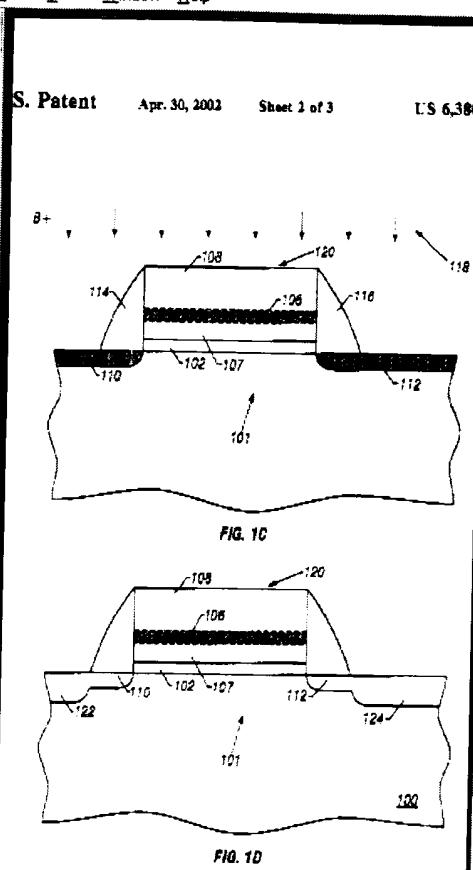
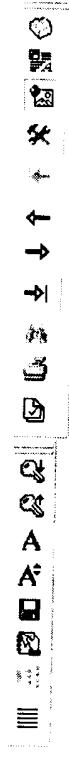
We claim:

1. A method of manufacturing a semiconductor device having an insulating film comprising the steps of:

preparing a semiconductor substrate having one of convexities and concavities which create a step height on a surface thereof; and

generating plasma by using organic silicon having tri- or more silazane bonding and oxidant and depositing a planarized insulating film on said semiconductor substrate by plasma chemical vapor deposition at a substrate temperature of about 100 degree C. or lower in order to significantly reduce

	U	1	Document ID	Issue Date	Pages	Title
21	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5567661 A	19961022	12	Formation of planarized insula film by plasma-enhanced CVI
22	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5508368 A	19960416	15	Ion beam process for deposit

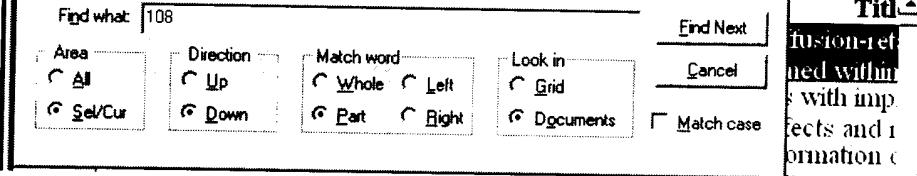


small amount of the dopant is preferably implanted into the region of the barrier region 106 or into the polysilicon region 107 layer below the barrier region 106. Through subsequent heat treatment operations (e.g., implant anneal steps) the barrier region 106 retards the downward diffusion of the boron and helps prevent boron from reaching the gate dielectric 102/polysilicon 104 interface. Alternatively, the dopant profile may exist entirely within the upper layer of polysilicon 108. In either case, the barrier region 106 retards the downward diffusion of dopant toward the gate dielectric 102 and toward the channel region 101.

In another embodiment generally following the sequence depicted in FIGS. 1A-1D, the barrier region 106 may be formed by depositing a nitrogen-contain layer, such as silicon nitride or titanium nitride, onto the top surface of polysilicon 104. Suitable conditions for depositing a layer of silicon nitride are low pressure chemical vapor deposition (LPCVD), plasma deposition and deposition by sputtering. Suitable conditions for depositing a layer of titanium nitride are low pressure chemical vapor deposition, plasma deposition and deposition by sputtering. Such a layer of titanium nitride is electrically conductive and thus helps ensure a good electrical connection between polysilicon region 107 and polysilicon region 108.

FIG. 3 is a cross-sectional view of a multi-layer structure incorporating three polysilicon layers, with a separate barrier region between each adjacent

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- 2 (240) dielectric adj (layer film) with (silicon adj containing)
- 2 (57) (dielectric adj (layer film) with (silicon adj containing)) and ((silicon react\$3 gas\$2)..
- 2 (57) ((dielectric adj (layer film) with (silicon adj containing)) and ((silicon react\$3 gas\$2..
- 2 (302) (silicon adj containing) with (dielectric near3 (layer film))
- 2 (148) ((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (sour..
- 2 (3) (((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (sour..
- 2 (3) ((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with silazane)
- 2 (0) (silicon adj containing) with (silicon with silazane)
- 2 (63) (silicon adj containing) with (silicon with silazane)
- 2 (4) ((silicon adj containing) with (silicon with silazane)) and nitridi\$4
- 2 (1) "6197628".PN.
- 2 (1) "5637527".PN.
- 2 (708) (silicon with silazane)
- 2 (7) dielectric with (silicon with silazane)
- 2 (18) dielectric with (silicon with nitridizing)
- 2 (116) semiconductor and (silicon with silazane)
- 2 (63) ((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (silaz..
- 2 (9) (((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (silaz..

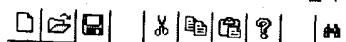
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dielectric adj (layer film)
with (silicon adj containing)



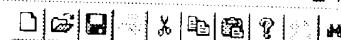
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- ⌚ (240) dielectric adj (layer film) with (silicon adj containing)
- ⌚ (57) (dielectric adj (layer film) with (silicon adj containing)) and ((silicon react\$3 gas\$2)..)
- ⌚ (57) ((dielectric adj (layer film) with (silicon adj containing)) and ((silicon react\$3 gas\$2..))
- ⌚ (302) (silicon adj containing) with (dielectric near3 (layer film))
- ⌚ (148) ((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (sour..)
- ⌚ (3) (((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (sour..))
- ⌚ (3) ((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with silazane)
- ⌚ (0) (silicon adj containing) with (silicon with silazane)
- ⌚ (63) (silicon adj containing) with (silicon with silazane)
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- ⌚ (1) "6197628".PN.
- ⌚ (1) "5637527".PN.
- ⌚ (708) (silicon with silazane)
- ⌚ (18) dielectric with (silicon with nitridizing)
- ⌚ (116) semiconductor and (silicon with silazane)
- ⌚ (63) ((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (silaz...))
- ⌚ (9) (((silicon adj containing) with (dielectric near3 (layer film))) and (silicon with (silaz...))

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1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030008989	20030109	12	Polymer synthesis and films therefrom	526/227	526/219; 526/303.1;
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030006477	20030109	17	Porous materials	257/527	
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030004218	20030102	18	Porous materials	521/77	

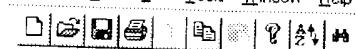


L9: (3) "10273667"

L10: (29) 6 and (\$20silazane with dielectric)

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	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
17	<input type="checkbox"/>	<input type="checkbox"/>	US 6426127 B1	20020730	12	Electron beam modification of perhydrosilazane spin-on glass	427/503	427/496;
18	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6383466 B1	20020507	11	Method of dehydroxylating a hydroxylated material and method of	423/335	427/497;
19	<input type="checkbox"/>	<input type="checkbox"/>	US 6245690 B1	20010612	22	Method of improving moisture	438/780	516/100;
20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6074939 A	20000613	14	Method for fabricating semiconductor device	438/596	516/111;
21	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6066573 A	20000523	5	Method of producing dielectric film	438/778	257/E21.263;
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6061112 A	20000509	13	Method of fabricating a reflection type liquid crystal display in which the	349/113	257/E21.277;
23	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6007878 A	19991228	25	Process for producing an optical recording medium having a protective	427/562	257/E21.703;
24	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5940154 A	19990817	12	Reflection type liquid crystal display and method of fabricating the same	349/113	257/E27.112;
25	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5393815 A	19950228	7	Silazane-based, heat resistant, dielectric coating compositions	524/262	257/632;
26	<input type="checkbox"/>	<input type="checkbox"/>	US 5318928 A	19940607	4	Method for the surface passivation of sensors using an in situ sputter cleani	438/50	257/404;
27	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5254411 A	19931019	9	Formation of heat-resistant dielectric coatings	428/447	524/430;
28	<input type="checkbox"/>	<input type="checkbox"/>	US 4719125 A	19880112	6	Cyclosilazane polymers as dielectric films in integrated circuit fabrication	438/780	257/E21.26;
29	<input checked="" type="checkbox"/>	<input type="checkbox"/>	DE 3490007 T	19840605		Semiconductor device mfr. using polysilazane coating - patterned then		257/E21.266;



L9: (3) "10273667"

L10: (29) 6 and (\$20silazane with dielectric)

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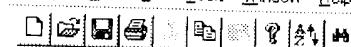
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5	<input type="checkbox"/>	US 20030030082	20030213	8	Method of forming an ultra thin dielectric film and a semiconductor device	257/288	257/324;
		A1			METHOD FOR FABRICATING A LOW DIELECTRIC CONSTANT	438/623	257/411;
6	<input checked="" type="checkbox"/>	US 20020182849	20021205	5	Method of filling a concave portion with an insulating material	438/618	438/197
		A1			Layered hard mask and dielectric materials and methods therefor	430/328	427/596;
7	<input type="checkbox"/>	US 20020182845	20021205	17	Method of forming an interlayer dielectric film	438/694	430/311;
8	<input type="checkbox"/>	US 20020172898	20021121	9	Method for forming a dielectric layer and semiconductor device incorporating	257/405	
		A1			Method of forming interlevel dielectric layer of semiconductor devi	438/623	438/626;
9	<input type="checkbox"/>	US 20020160614	20021031	16	Method of improving moisture resistance of low dielectric constant fi	427/569	438/631
		A1			Method of forming interlevel dielectric layer of semiconductor devi	438/738	
10	<input type="checkbox"/>	US 20020135031	20020926	15	Method of improving moisture resistance of low dielectric constant fi	438/758	438/624;
		A1			Surface modifying layers for organic thin film transistors	257/40	257/E21.263;
11	<input type="checkbox"/>	US 20020064936	20020530	6	Electron beam modification of perhydrosilazane spin-on glass	427/503	257/E21.277;
		A1					257/411;
12	<input type="checkbox"/>	US 20020043695	20020418	9			438/99
		A1					427/496;
13	<input type="checkbox"/>	US 20010026849	20011004	23			427/497;
		A1					516/100;
14	<input type="checkbox"/>	US 6479399 B2	20021112	6			
15	<input type="checkbox"/>	US 6448187 B2	20020910	22			
16	<input checked="" type="checkbox"/>	US 6433359 B1	20020813	11			
17	<input type="checkbox"/>	US 6426127 B1	20020730	12			
18		TIC 6282166 B1	20020507	11			

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L9: (3) "10273667"

L10: (29) 6 and (\$20silazane with dielectric)

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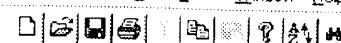
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		A1			Method of improving moisture resistance of low dielectric constant film	438/780	257/642;
2	<input type="checkbox"/>	US 20030054667	20030320	24	Electronic devices and methods of manufacture	438/400	438/623;
		A1			Dielectric layer for semiconductor device having less current leakage and	257/200	438/638
3	<input checked="" type="checkbox"/>	US 20030054616	20030320	27	Method of forming an ultra thin dielectric film and a semiconductor device	257/288	257/324;
		A1			METHOD FOR FABRICATING A LOW DIELECTRIC CONSTANT	438/623	257/411;
4	<input type="checkbox"/>	US 20030052338	20030320	14	Method of filling a concave portion with an insulating material	438/618	438/197
		A1			Layered hard mask and dielectric materials and methods therefor	430/328	427/596;
5	<input type="checkbox"/>	US 20020182849	20021205	5	Method of forming an interlayer dielectric film	438/694	430/311;
		A1			Method for forming a dielectric layer and semiconductor device incorporating	257/405	
6	<input checked="" type="checkbox"/>	US 20020182845	20021205	17	Method of forming interlevel dielectric layer of semiconductor device	438/623	438/626;
		A1			Method for forming an ultra thin dielectric film and a semiconductor device	257/435	438/631
7	<input type="checkbox"/>	US 20020172898	20021121	9	Method of improving moisture resistance of low dielectric constant film	427/569	118/723R;
		A1			Method of forming an interlevel dielectric layer of semiconductor device	427/570	427/376.2;
8	<input type="checkbox"/>	US 20020160614	20021031	16	Method of forming an interlayer dielectric film	427/570	428/622;
		A1			Method for forming a dielectric layer and semiconductor device incorporating	257/405	
9	<input type="checkbox"/>	US 20020135031	20020926	15	Method of forming interlevel dielectric layer of semiconductor device	438/623	
		A1			Method for forming an ultra thin dielectric film and a semiconductor device	257/435	
10	<input type="checkbox"/>	US 20020064936	20020530	6	Method of improving moisture resistance of low dielectric constant film	427/569	
		A1			Method of forming an interlevel dielectric layer of semiconductor device	427/570	
11	<input type="checkbox"/>	US 20020043695	20020418	9	Method of forming an interlevel dielectric layer of semiconductor device	427/570	
		A1			Method for forming a dielectric layer and semiconductor device	257/405	
12	<input type="checkbox"/>	US 20010026849	20011004	23	Method of improving moisture resistance of low dielectric constant film	427/569	
		A1			Method of forming an interlevel dielectric layer of semiconductor device	427/570	
13	<input type="checkbox"/>	US 6170200 D3	20021112	6	Method of forming an interlevel dielectric layer of semiconductor device	427/570	
					Method for forming a dielectric layer and semiconductor device	257/405	
14	<input type="checkbox"/>				Method of forming an interlevel dielectric layer of semiconductor device	427/570	



- ✓ L7: (70) 6 and (\$20silazane with deposit\$3)
- ✓ L10: (29) 6 and (\$20silazane with dielectric)

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U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
14	<input type="checkbox"/>	US 20020054565 A1	20020509	12	Optical information medium and its testing method	369/283	264/1.33; 264/1.7;
15	<input type="checkbox"/>	US 20020043695 A1	20020418	9	Method for forming an ultra thin dielectric film and a semiconductor	257/435	
16	<input type="checkbox"/>	US 20020032073 A1	20020314	15	HIGHLY DURABLE AND	473/324	473/349
17	<input type="checkbox"/>	US 20020025658 A1	20020228	9	ABRASION RESISTANT COMPO		
					Method for forming a barrier layer	438/486	
18	<input type="checkbox"/>	US 20020015135 A1	20020207	32	Image projection system with a polarizing beam splitter	353/31	
19	<input type="checkbox"/>	US 20010034076 A1	20011025	16	Process for wafer level treatment to reduce stiction and passivate microm	438/50	438/780
20	<input type="checkbox"/>	US 20010029114 A1	20011011	6	Method of forming polymeric layers of silicon oxynitride	438/794	438/790
21	<input type="checkbox"/>	US 20010026849 A1	20011004	23	Method of improving moisture resistance of low dielectric constant fi	427/569	118/723R; 427/376.2;
22	<input type="checkbox"/>	US 6501014 B1	20021231	15	Coated article and solar battery module	136/256	136/251; 257/434;
23	<input type="checkbox"/>	US 6479399 B2	20021112	6	Method of forming interlevel dielectric layer of semiconductor devi	438/738	438/623; 438/624;
24	<input type="checkbox"/>	US 6475883 B2	20021105	9	Method for forming a barrier layer	438/486	438/398; 438/652
25	<input type="checkbox"/>	US 6448187 B2	20020910	22	Method of improving moisture resistance of low dielectric constant fi	438/758	257/E21.263; 257/E21.277;
26	<input type="checkbox"/>	US 6447120 B2	20020910	33	Image projection system with a polarizing beam splitter	353/20	359/486
27		US 6436137 B1	20020725				

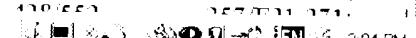
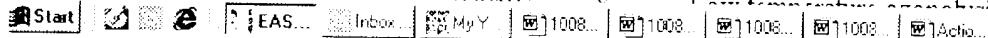


L7: (70) 6 and (820) silazane with deposit#3

110: (28) 6 and (\$20silazane with deposit\$3)

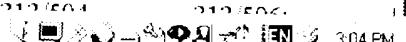
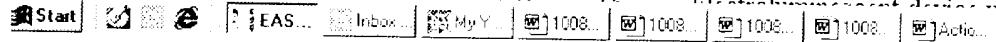
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U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
27	<input checked="" type="checkbox"/>	US 6426127 B1	20020730	12	Electron beam modification of perhydrosilazane spin-on glass	427/503	427/496;
28	<input checked="" type="checkbox"/>	US 6410968 B1	20020625	8	Semiconductor device with barrier layer	257/412	427/497;
29	<input checked="" type="checkbox"/>	US 6379988 B1	20020430	24	Pre-release plastic packaging of MEMS and IMEMS devices	438/51	438/106;
30	<input checked="" type="checkbox"/>	US 6335224 B1	20020101	11	Protection of microelectronic devices during packaging	438/114	438/115;
31	<input checked="" type="checkbox"/>	US 6245690 B1	20010612	22	Method of improving moisture resistance of low dielectric constant film	438/780	438/460;
32	<input checked="" type="checkbox"/>	US 5976466 A	19991102	34	Multiple-probe diagnostic sensor	422/82.11	257/E21.263;
33	<input checked="" type="checkbox"/>	US 5776603 A	19980707	8	Glazing pane equipped with at least one thin film and method of manufacture	428/336	250/461.1;
34	<input checked="" type="checkbox"/>	US 5733611 A	19980331	14	Method for densification of porous billets	427/591	359/580;
35	<input checked="" type="checkbox"/>	US 5679413 A	19971021	15	Highly abrasion-resistant, flexible coatings for soft substrates	427/534	359/586;
36	<input checked="" type="checkbox"/>	US 5618619 A	19970408	14	Highly abrasion-resistant, flexible coatings for soft substrates	428/334	427/527;
37	<input checked="" type="checkbox"/>	US 5380553 A	19950110	14	Reverse direction pyrolysis processing	427/226	427/534;
38	<input checked="" type="checkbox"/>	US 5322913 A	19940621	30	Polysilazanes and related compositions, processes and uses	528/15	427/126.1;
39	<input checked="" type="checkbox"/>	US 5318928 A	19940607	4	Method for the surface passivation of sensors using an in situ sputter cleani	438/50	264/239;
40	<input checked="" type="checkbox"/>	US 5218857 A	19940607	8			264/280;





- ☒ L7: (70) 6 and (\$20silazane with deposit\$3)
- ☒ L10: (29) 6 and (\$20silazane with dielectric)





L7: (70) 6 and (\$20silazane with deposit\$3)

L10: (29) 6 and (\$20silazane with dielectric)

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U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
52	<input type="checkbox"/>	US 4952715 A	19900828	15	Polysilazanes and related compositions, processes and uses	556/409	528/15;
53	<input type="checkbox"/>	US 4950950 A	19900821	16	Electroluminescent device with silazane-containing luminescent zone	313/504	528/28
54	<input type="checkbox"/>	US 4911992 A	19900327	18	Platinum or rhodium catalyzed multilayer ceramic coatings from hyd	428/698	313/506;
55	<input type="checkbox"/>	US 4898907 A	19900206	8	Compositions of platinum and rhodium catalyst in combination with	524/490	428/690;
56	<input type="checkbox"/>	US 4863755 A	19890905	24	Plasma enhanced chemical vapor deposition of thin films of silicon nitri	427/574	427/122;
57	<input type="checkbox"/>	US 4822697 A	19890418	13	Platinum and rhodium catalysis of low temperature formation multilayer	428/698	427/126.2;
58	<input type="checkbox"/>	US 4808653 A	19890228	8	Coating composition containing hydrogen silsesquioxane resin and ot	524/398	106/287.1;
59	<input type="checkbox"/>	US 4756977 A	19880712	13	Multilayer ceramics from hydrogen silsesquioxane	428/704	106/287.14;
60	<input type="checkbox"/>	US 4753856 A	19880628	15	Multilayer ceramic coatings from silicate esters and metal oxides	428/698	257/E21.266;
61	<input type="checkbox"/>	US 4753855 A	19880628	14	Multilayer ceramic coatings from metal oxides for protection of electro	428/702	257/E21.271;
62	<input type="checkbox"/>	US 4751191 A	19880614	8	Method of fabricating solar cells with silicon nitride coating	438/72	257/E21.502;
63	<input type="checkbox"/>	US 4749631 A	19880607	17	Multilayer ceramics from silicate esters	428/704	257/E23.118;
64	<input type="checkbox"/>	US 4719125 A	19880112	6	Cyclosilazane polymers as dielectric films in integrated circuit fabrication	438/780	136/256;

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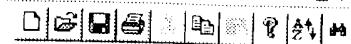
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L7: (70) 6 and (\$20silazane with deposit\$3)
 L10: (29) 6 and (\$20silazane with dielectric)

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58	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4808653 A	19890228	8	Coating composition containing hydrogen silsesquioxane resin and of Multilayer ceramics from hydrogen silsesquioxane	524/398	106/287.1; 106/287.14; 257/E21.266; 257/E21.271; 257/E21.266; 257/E21.271; 257/E21.502; 257/E23.118; 136/256; 136/258; 136/256; 257/E21.271; 257/E21.263; 257/E21.271; 216/40; 427/489; 204/165; 427/488; 204/192.32; 427/489; 136/256; 257/E21.174;
59	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4756977 A	19880712	13	Multilayer ceramic coatings from silicate esters and metal oxides	428/704	
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61	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4753855 A	19880628	14	Method of fabricating solar cells with silicon nitride coating	428/702	
62	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4751191 A	19880614	8	Multilayer ceramics from silicate esters	438/72	
63	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4749631 A	19880607	17	Cyclosilazane polymers as dielectric films in integrated circuit fabrication	428/704	
64	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4719125 A	19880112	6	Use of plasma polymerized organosilicon films in fabrication of li	438/780	
65	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4599243 A	19860708	11	Use of plasma polymerized organosilicon films in fabrication of li	216/18	
66	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4562091 A	19851231	9	Use of plasma polymerized organosilicon films in fabrication of li	427/489	
67	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4493855 A	19850115	10	Method of fabricating solar cells	438/670	
68	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 4451969 A	19840605	9	Formation of silicon nitride film for semiconductor device, involves suppl	438/62	
69	<input type="checkbox"/>	<input checked="" type="checkbox"/>	WO 200217374 A	20020228	60	Plasma-deposited abrasion-resistant coating prodn. - using as plasma mon		
70	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	EP 252870 A	19880113				